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PATENT
81790.0214
RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2815

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of Art Unit 2815
Tomomi MOMOHARA Examiner: Jesse A. Fenty
Serial No.: 09/511,620

Filed: February 23, 2000

For: SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE HAVING A
PLURALITY OF WELLS,
TEST METHOD OF TESTING
THE SEMICONDUCTOR
WHICH EXECUTES THE TEST METHOD

I hereby certify that this correspondence is
being facsimile transmitted to the United
States Patent and Trademark Office, Fax No.
703 746-3892 on March 19, 2003.

Sterling A. Mason Reg. No. 41,179
Name
Signature Date
March 19, 2003

RESPONSE TO FINAL OFFICE ACTION

Assistant Commissioner for Patents
Washington, D.C. 20231
Attention: Box AF

FAX RECEIVED

MAR 20 2003

TECHNOLOGY CENTER 2800

Dear Sir:

Pursuant to 37 C.F.R. § 1.116, Applicant submits the following Requested Amendment and Remarks in response to the Final Office Action mailed December 20, 2002, the three-month statutory period for responding expiring March 20, 2003. Applicant respectfully asserts that entry of the Requested Amendment would place the application in condition for allowance. Alternatively, entry of the Requested Amendment would place the application in better form for consideration on appeal.